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PTO-1449

Application No. 09/809,731

Applicant(s) Giles, et al.

Docket Number 068167.0107

Group Art Unit 2132

Filing Date 3/15/01

**Information Disclosure Citation in an Application**

**U.S. PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
M.S.	A	5278973	1/11/94	O'Brien et al.	395	500	6/27/91
M.S.	B	5301277	4/5/94	Kanai	709	301	12/23/91
M.S.	C	5367628	11/22/94	Ote et al.	345	501	10/15/92
M.S.	D	5448264	9/5/95	Pinedo et al.	345	508	6/14/93
M.S.	E	5452456	9/19/95	Mourey et al.	713	100	12/18/92
M.S.	F	5502809	3/26/96	Takano	345	509	9/6/94
M.S.	G	5640562	6/17/97	Wold et al.	395	652	2/27/95
M.S.	H	5666521	9/9/97	Marisetty	345	525	11/20/96
M.S.	I	5742797	4/21/98	Celi, Jr. et al.	345	507	8/11/95
M.S.	J	5752275	5/12/98	Hammond	711	207	7/14/97
M.S.	K	5757386	5/26/98	Celi, Jr. et al.	345	507	8/11/95
M.S.	L	5790825	8/4/98	Traut	712	209	8/5/97
M.S.	M	5831607	11/3/98	Brooks	345	333	1/25/96
M.S.	N	5860147	1/12/99	Gochman et al.	711	207	9/16/96
M.S.	O	5940872	8/17/99	Hammond et al.	711	207	11/1/96
M.S.	P	6014170	1/11/00	Pont et al.	348	232	6/20/97
M.S.	Q	6026476	2/15/00	Rosen	711	206	8/6/97
M.S.	R	6067618	5/23/00	Weber	713	1	3/26/98

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

**NON-PATENT DOCUMENTS**

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	
M.S.	S	"Processor Instruction Sets," The PC Guide, version date 12/18/00, <a href="http://www.pcguides.com/ref/cpu/arch/int/inst-c.html">http://www.pcguides.com/ref/cpu/arch/int/inst-c.html</a> .	
M.S.	T	"M68060 User's Manual," Motorola, 1994, pp. i-xviii; Section 4, Memory Management Unit, pp. 4-1 to 4-30, <a href="http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf">http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf</a> .	
M.S.	U	"MPC750, RISC Microprocessor User's Manual," Motorola, 8/97, Contents, pp. iii-xvi; Chapter 5, Memory Management, pp. 5-1 to 5-34; Glossary, pp. Glossary-1 to Glossary-13, <a href="http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf">http://e-www.motorola.com/bdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf</a> .	

EXAMINER *M. S. Shoukri*

DATE CONSIDERED 10/13/04

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U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
MS	A	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96
MS	B	4,779,188	10/18/88	Gum et al.	364	200	10/19/87
FOREIGN PATENT DOCUMENTS						RECEIVED APR 04 2002 Technology Center	
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
MS	C	EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X
NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					
MS	D	PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.					
MS	E	Traut E, "Building the Virtual PC," Byte, McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.					
MS	F	"Intel386 DX Microprocessor," Intel, pp. 32-58, 31 December 1995.					
MS	G	"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL: www.btech.co/changingchips.html, paragraphs '00061-'00071, retrieved 12/10/01.					
MS	H	"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.					
MS	I	Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," IBM Systems Journal, Vol. 30, No. 1, pp. 34-51, 1991.					
MS	J	Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," Microprocessing and Microprogramming, Vol. 22, No. 2, pp. 81-96, February 1988.					
EXAMINER				DATE CONSIDERED			
				10/13/04			
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